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APPLICATION NO.	F	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/740,017	12/20/2000		Shi-Tron Lin	6484.0073	4272
22852	7590	12/17/2003		EXAMINER	
	N, HEND	ERSON, FARAB	ROSE, KIESHA L		
LLP 1300 I STREET, NW WASHINGTON, DC 20005				ART UNIT	PAPER NUMBER
				2822	

DATE MAILED: 12/17/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	licant(s)					
Office Audion Occurs	09/740,017	LIN, SHI-TRON					
Office Action Summary	Examiner	Art Unit					
	Kiesha L. Rose	2822					
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the c	correspondence address					
A SHORTENED STATUTORY PERIOD FOR REPL' THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period v - Failure to reply within the set or extended period for reply will, by statute - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).  Status	36(a). In no event, however, may a reply be tin y within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from , cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).					
1) Responsive to communication(s) filed on 24 S	<u>eptember 2003</u> .						
2a) ☐ This action is <b>FINAL</b> . 2b) ☑ This	action is non-final.						
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims							
4) Claim(s) is/are pending in the application.							
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>1-19 and 101,102 and 104</u> is/are rejected.							
7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/o	r election requirement						
	r election requirement.						
Application Papers							
9) The specification is objected to by the Examiner.							
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority under 35 U.S.C. §§ 119 and 120							
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a) All b) Some * c) None of:  1. Certified copies of the priority document  2. Certified copies of the priority document  3. Copies of the certified copies of the priority document  * See the attached detailed Office action for a list  13) Acknowledgment is made of a claim for domesti since a specific reference was included in the first  37 CFR 1.78.  a) The translation of the foreign language pro  14) Acknowledgment is made of a claim for domesti reference was included in the first sentence of the	s have been received. s have been received in Application rity documents have been received in (PCT Rule 17.2(a)). of the certified copies not received priority under 35 U.S.C. § 119(a) st sentence of the specification of the covisional application has been received priority under 35 U.S.C. §§ 120	ion No  ed in this National Stage  ed.  e) (to a provisional application)  r in an Application Data Sheet.  eeived.  and/or 121 since a specific					
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) 🔲 Notice of Informal F	(PTO-413) Paper No(s) Patent Application (PTO-152)					

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## **DETAILED ACTION**

This Office Action is in response to the RCE filed 24 September 2003.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim1-19, 101, 102 and 104 are rejected under 35 U.S.C. 102(b) as being anticipated by Lin (U.S. Patent 5,721,439).

Lin discloses a MOS transistor with an ESD protection circuitry (Figs. 5c, 7c and 8) comprising a semiconductor layer comprising a layer of silicon formed on an insulting layer (41) to form a silicon-on-insulator substrate (40), a first diffusion region (48) formed in the layer, a second diffusion layer (47) formed in the layer adjacent to and spaced apart from the first region, a channel region formed at a boundary between the first and second diffusion regions, wherein channel region is bounded by the first and second diffusion edges of the first diffusion region, at least one island formed at least partially in the first diffusion region and being positioned non-symmetrically about a plane perpendicular to a top surface of the semiconductor layer and perpendicular to the boundary, wherein plane bisects the channel region and is parallel to the first and second diffusion regions, wherein islands (63-65 and 81-86) comprise either polysilicon

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or a field oxide (insulation material) and comprise a shallow trench island and at least one island comprises a first and second island where the first is substantially longer then the second and a gap between proximate ends of the first and second being near an edge of the first diffusion region, a third and fourth island being formed along a length direction of the second and the third being longer than the fourth and the ends are joined by a first connection. The ESD device has a MOSFET, the first diffusion is a drain, the second is a source and comprising a gate overlaying the source and drain, the ESD protection device is a bipolar structure where the first diffusion is a collector region and second is an emitter.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kiesha L. Rose whose telephone number is 571-272-1844. The examiner can normally be reached on M-F 8:30-6:00 off 2nd Mondays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

